Backward Bounded Model Checking in CPAchecker

Bas Laarakker

Google Summer of Code '23

2023-09-11 @ CPAchecker Workshop





About Me

Currently MSc Logic @ ILLC, University of Amsterdam
 Interested in mathematical logic and theoretical CS
 Participant in GSoC '23 @ SoSy Lab, LMU Munich
 Summer program for contributing to open-source software
 Supervised by Nian-Ze Lee

Improved support for any backward analysis in CPACHECKER

- Improved support for any backward analysis in CPACHECKER
- ▶ Implemented backward BMC algorithm in CPACHECKER
 - Handling pointer aliasing improves performance
 - Backward BMC can complement regular BMC

- Improved support for any backward analysis in CPACHECKER
- ▶ Implemented backward BMC algorithm in CPACHECKER
 - Handling pointer aliasing improves performance
 - Backward BMC can complement regular BMC
- Empirical evidence for proof that backward BMC and (plain) k-induction are equivalent [2]

- Improved support for any backward analysis in CPACHECKER
- ▶ Implemented backward BMC algorithm in CPACHECKER
 - Handling pointer aliasing improves performance
 - Backward BMC can complement regular BMC
- Empirical evidence for proof that backward BMC and (plain) k-induction are equivalent [2]
- Want to identify limiting factors of backward BMC performance

Backward formula construction and SMT solving

Backward Bounded Model Checking

Backward Analysis

- We study the error location reachability problem
 - Forward analysis unrolls a program starting from the main entry to the error locations
 - Backward analysis unrolls a program starting from the error locations to the main entry

Backward Bounded Model Checking

- Based on paper Backward Symbolic Execution with Loop Folding, M. Chalupa and J. Strejček [2]
- They claim that BBMC is equivalent to k-induction by showing:
 - For a CFA A, let P be the set of all satisfiable paths from the error location. Then both algorithms, when executed on CFA A,
 - return FALSE if P contains a path to the entry location;
 - return TRUE if *P* is finite and contains no path to the entry location;
 - do not terminate if P is infinite and contains no path to the entry location.

Example: k-Induction



Example: BBMC



Backward Analysis in CPACHECKER

Backward Analysis in CPACHECKER

Some support for backward analysis exists:
 Backward PredicateCPA (partial support)
 Backward formula construction
 Backward CallstackCPA
 Backward LeastianCDA

Backward LocationCPA

Backward Analysis in CPACHECKER

Some support for backward analysis exists:

- Backward PredicateCPA (partial support)
 - Backward formula construction
- Backward CallstackCPA
- Backward LocationCPA
- Missing support for backward analysis:
 - Exporting witness from backward analysis
 - Backward LoopBoundCPA
 - Handling of pointer aliasing by PredicateCPA

Contributions to CPACHECKER

- Implemented support for backward analysis for WitnessExporter
 - MR1

Contributions to CPACHECKER

 Implemented support for backward analysis for WitnessExporter

MR1

Implemented support for LoopBoundCPA and the BBMC algorithm in the BackwardBMCAlgorithm class

MR2

Contributions to CPACHECKER

 Implemented support for backward analysis for WitnessExporter

MR1

Implemented support for LoopBoundCPA and the BBMC algorithm in the BackwardBMCAlgorithm class

MR2

 Implemented support for handling pointer aliasing for backward analysis (WIP)

MR3

- Some language features are not yet supported:
 - union
 - Pointers to struct

Experimental Setup

- On revision 44369 of CPACHECKER
 - Branch backward-bmc-algorithm
- Algorithms:
 - BBMC (pointer aliasing disabled)
 - BBMC+PA (pointer aliasing enabled)
 - BMC
 - Plain k-induction
- ▶ 5652 *ReachSafety* programs of SV-COMP '23 [1]
 - Removed programs with unsupported features for backward pointer aliasing
 - ProductLines
 - LinuxDrivers

Results

Algorithm	BBMC	BBMC+PA
Correct True	727	959
Correct False	1152	1113
Incorrect True	10	47
Incorrect False	383	7

Table: Results of BBMC on 5652 ReachSafety programs

- Adding support for backward pointer aliasing solves most false alarms and improves performance by 11%
- ► However, we get more false proofs
 - These programs have unsupported language features

Quantile Plot: All Tasks



Quantile Plot: ECA Tasks



BBMC on ECA Solver Comparison



Observations

- BBMC complements BMC on certain program sets
- BBMC and k-induction achieve their results at the same loop unrolling bound
- Their results differ only when one algorithm times out before reaching the "required" bound to solve the program
 - Suggests that BBMC and k-induction are equivalent on ReachSafety-ECA
- On certain programs, BBMC takes a lot of time compared to k-induction
 - Likely due to SMT solving phase
 - Solvers make a difference

Conclusion

- Implemented BBMC and further support for backward analysis
- BBMC performs quite well, especially on certain subsets
 - BBMC and k-induction seem to be equivalent, at least on ReachSafety-ECA
 - BBMC can benefit from faster performance
- Future work:
 - Identify bottlenecks of BBMC performance (backward formula construction?)
 - Evaluate BBMC vs k-induction further

Technical Challenges

JavaScript error when generating report

TypeError: simplifiedGraphMap is undefined

- Improve support for backward pointer aliasing
 - 🕨 union
 - Pointers to struct

```
1 struct Point { int x; };
2 // Expected result: FALSE
3 int test1() {
  struct Point p;
4
5 struct Point *pt = &p;
6 pt->x = 2;
7 p.x = pt - x - 1;
8 if (p.x == 1) {
   reach error();
9
                                9
      return -1;
10
                               10
11
     }
                               11
    return 0;
12
                               12 }
13 }
```

```
1 union Data { int a; int b; };
2 // Expected result: FALSE
3 int test2() {
4 union Data d;
5 d.a = 6;
6 d.b = d.a + 1;
7 if (d.b == 7) {
8 reach_error();
9 return -1;
10 }
11 return 0;
```

References I



Dirk Beyer.

Competition on software verification and witness validation: Sv-comp 2023.

In Sriram Sankaranarayanan and Natasha Sharygina, editors, <u>Tools and</u> Algorithms for the Construction and Analysis of Systems, pages 495–522, Cham, 2023. Springer Nature Switzerland.

Marek Chalupa and Jan Strejček.

Backward symbolic execution with loop folding.

In Cezara Drăgoi, Suvam Mukherjee, and Kedar Namjoshi, editors, <u>Static</u> Analysis, pages 49–76, Cham, 2021. Springer International Publishing.